



		Created by spsop from cwo-ccc-a7lc		
17 1	6:39	Problem to access most front end hosts via diamon. A.Bland checking this. Created by spsop from cwo-ccc-a7lc		
18 1	6:45	UA9 interlock test: Scatter position -> ok!		
		20090618164609.png Created by spsop from cwo-ccc-a0lc		
19 1	7:03	ECN3 2 searches broken. Serge going to help make the patrol. Created by spsop from cwo-ccc-a7lc		
20 1	7 : 13	Magnet piquet checked magnet temperature interlock on SMD9. All ok for him, but the interlock still appears on the CIS software. Call PO piquet to check this (he is currently busy on the PSB). Created by spsop from cwo-ccc-a7lc		
21 1	7:30	v2nb81 front end chassis changed. We now seem to have full controls access to EA power supplies. TI checking auxilliary compensator. Created by spsop from cwo-ccc-a6lc		
22 1	8:20	BI people finished work on BLMs in LSS5. There is still some noise to be reduced on some of these monitors. Inform BI if 1 hour without beam. Created by spsop from cwo-ccc-a6lc		
23 1	8:59	Eda called: she askes to closed the shieldings. We close chain 6 for this. Ventillation tests overnight. Created by spsop from cwo-ccc-a6lc		
24 1	9:29	Vacuum people out of ring. Closing chain 1. Created by spsop from cwo-ccc-a6lc		
25 1	9:31	Call CO piquet. MTG events not arriving on north area hosts, v2nb80, v2nb81, v2nb82. Created by spsop from cwo-ccc-a6lc		
26 1	9:33	Valves open Created by spsop from cwo-ccc-a6lc		
27 1	9:47	TRX3 off, trying to restore it. Created by spsop from cwo-ccc-a6lc		
28 1	9:52	Piquet CO on site. Created by spsop from cwo-ccc-a6lc		
29 1	9:57	Running dry coast cycle for Thomas Bohl, RF timing. Created by spsop from cwo-ccc-a6lc		
30 2	0:00	> Piquet SPS > RF POW > Called Unable to restart TRX3, UG2 driver no 380V. Call RF piquiet C.Julie. Created by spsop from cwo-ccc-a		
31 2	0:15	> Piquet SPS > RF POW > On Site Created by spsop from cwo-ccc-a6lc		
32 2	0:33	TRX3 again available. Created by spsop from cwo-ccc-a6lc		
33 2	0:33	CO piquet checked timing cards on North area hosts, v2nb80,81,82. All functioning correctly, and no errors from diamon. It could be there is no timing set for the North area with the cycle we are currently running for the MD. So we are able to switch on pulsed converters in the north area,		

		but not set the currents.	Created by spsop from cwo-ccc-a6lc	
		> Piquet SPS > PO Ring > End	created by speep from two see acre	
34	20 : 45		Created by spsop from cwo-ccc-a6lc	
35	21:01	Problem with RF Rocs, which continues to pulse when we go into coast. We call first M.Jonker, then Stephen Page for advice. We find a configuration table for rocs under /user/pcsps/rocs/configuration/standard.evlist, which contains only RF configuration for pulsed mode. Stephen will edit this table later, so we can test if that is sufficient to cure the problem. Created by spsop from cwo-ccc-a6lc		
36	21:17	SIS interlock from blring_dump_veto_ba5 (Bearlier). Reboot blr50s	BI were working on BLMs in BA5 Created by spsop from cwo-ccc-a6le	
37	21:23	Mask the above SIS interlock.	Created by spsop from cwo-ccc-a6le	
38	21:41	Stephen Page arrived to check Rocs timing		
3.0	21:46	surveillance of BLRING in LSS5 unmasked ag		
	21.40	adjusting MKP timing.	Created by spsop from cwo-ccc-a6le	
40	21:51	Timing configuration edited for RF ROCS. We this to take effect.	-	
\dashv		> INFO > SHIFT SUMMARY	Created by spsop from cwo-ccc-a6le	
41	22:32	Vacuum intervention on UA9 delayed start with beam until 19:30. After some problems with RF ROCS timing when going into coast. Also trying to check E power supplies, for tomorrow's return to physics, but unable to set currents on pulsed functions with the timing of the MD cycle. CNGS ventillation tests overnight, do not cut chain 6.		
42	22:49	MPS trip, electrical ripple on SMQS. Reset	Created by spsop from cwo-ccc-a6le Created by spsop from cwo-ccc-a6le Created by spsop from cwo-ccc-a6le	
43	22 : 54	MPS trip again. See attached file.	Created by spsop from cwo-ccc-a7l	
44	22:58	MPS trip again after a third retry.		

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FAULTS

Group Fault Element Description Begin End Duration

NO FAULT





